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IN THE CLAIMS

1 - 20. (Cancelled)

21. (Presently Amended) A stackable block comprising:

a rigid member having a first surface with a protrusion thereon and a second surface having a receptacle;

wherein said protrusion for mechanically stacking one stackable block to the receptacle of another stackable block, and said receptacle for receiving the protrusion of another stackable member; and

a memory for storage of at least one computer program instruction, wherein when said plurality of stackable blocks are mechanically stacked, a plurality of instructions are electrically connected forming a computer program, for execution by a computer external to said block.

22. (Original) The stackable block of claim 21 wherein said rigid member has a plurality of protrusions and a plurality of receptacles.

23. (Original) The stackable block of claim 22 wherein said first surface and said second surface are substantially opposite to one another.

24. (Original) The stackable block of claim 23 wherein said memory is a non-volatile memory for the storage of at least one instruction.

25. (Original) The stackable block of claim 24 wherein said non-volatile memory stores a plurality of instructions.

26. (Original) The stackable block of claim 24 wherein said non-volatile memory further stores a copyright protected work.

27. (Original) The stackable block of claim 24 wherein said non-volatile memory is electrically connected to said plurality of protrusions.

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28. (Original) The stackable block of claim 24 wherein said non-volatile memory is electrically connected to said plurality of receptacles.
29. (Original) The stackable block of claim 23 wherein said rigid member is substantially rectilinearly shaped.
30. (Original) The stackable block of claim 22 wherein said plurality of protrusions and plurality of receptacles of each stackable block are asymmetrically shaped.
31. (Original) The stackable block of claim 21 further comprising:
a port for connecting to another stackable block;
wherein said another stackable block having a memory for storage of an (un)conditional branching computer program instruction; and
wherein said port for connecting to said (un)conditional branching computer program instruction.
32. (Original) The stackable block of claim 21 further comprising:
a port for connecting to another stackable block;
wherein said another stackable block having a memory for storage of a computer program instruction; and
wherein said port for connecting to said computer program instruction.
33. (Original) A block for a toy comprising:
a board having a first surface adapted to fit into a stack of one or more blocks each block having non-volatile memory for the storage of one or more computer program instructions forming a computer program; and
a computer, in said board, for receiving said computer program from said stack when said stack is fitted to said board, and for executing said computer program.
34. (Original) The block of claim 33 wherein said board having a plurality of protrusions on said first surface, said protrusions adapted to fit into said stack.

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35. (Original) The block of claim 33 wherein said board having a plurality of receptacles on said first surface, said receptacles adapted to fit into said stack.
36. (Original) The block of claim 33 further comprising a compiler associated with said computer for compiling said computer program from said stack to generate a compiled computer program and for executing said compiled computer program.
37. (Original) The block of claim 33 further comprising an interpreter associated with said computer for interpreting said computer program from said stack to generate an interpreted computer program and for executing said interpreted computer program.